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PATENT

Attorney Docket No. AUS920030132US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Michael Norman Day et al.

Title: METHOD AND APPARATUS FOR COHERENT MEMORY STRUCTURE OF

HETEROGENEOUS PROCESSOR SYSTEMS

Serial No.: To be assigned

Filed: Herewith

Group: To be assigned

Commissioner for Patents PO Box 1450 Alexandria, Virginia 22313-1450

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

In compliance with the duty of disclosure set forth in 37 C.F.R. ' 1.56, Applicants wish to bring the items cited in the attached Form PTO/SB/08 to the attention of the Examiner. A copy of each item is enclosed for the convenience of the Examiner.

No representation is made, and no representation is intended, that more relevant material does not exist, or that the order of presentation of this material in any way reflects the relative pertinence. The references cited are not intended to constitute an admission of any kind. Specifically, this presentation is not an admission that the items listed in the attached Form PTO/SB/08 are properly citable against the above-identified application as prior art.

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Applicants consider the invention to be distinguishable over the references cited.

As this Information Disclosure Statement is being filed before the issuance of the first office action, no fee is enclosed.

Respectfully submitted,

Date: 10/8/05

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT						APPLICANT: Day et al.			EXAMINER: To be assigned			
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